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Attorney's Docket No.: Intel 10559-
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In the Specification:

Please insert the following new paragraph prior to the
Background commencing at page 1, line 4:

This application is a continuation of U.S. Patent No.
6,697,888, entitled "BUFFERING AND INTERLEAVING DATA TRANSFER
BETWEEN A CHIPSET AND MEMORY MODULES," which issued from U.S.
Application Serial No. 09/675,304, filed September 29, 2000, the
contents of which are incorporated herein by reference.